

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#4/Amend B
L. Johnson
3/22/00

In re application of

HIEP V. TRAN

Serial No. 08/938,163 (TI-19819)

Filed September 26, 1997



For: SELF SELECT PRECHARGED DOMINO LOGIC CIRCUIT

Art Unit 2819

Examiner Don Le

Assistant Commissioner for Patents
Washington, D. C. 20231

Sir:

SUPPLEMENTAL AMENDMENT

In response to a telephone conference with the Examiner, please amend the above identified application as follows:

In the claims:

Amend claim 28 as follows:

28. (Amended) A precharge circuit for a dynamic CMOS [logic] circuit as recited in claim 27 wherein a gate of said keeper transistor is connected to an output of said dynamic logic circuit.

REMARKS

As kindly noted by the Examiner in a telephone conference, claim 28 as allegedly copied from Patent No. 5,831,452 (claim 11 thereof) was incorrectly copied. This amendment corrects this error.

Action as requested in the prior amendment is respectfully requested.

Respectfully submitted,


Jay M. Cantor

Reg. No. 19906

(202) 639-7713

640 28 19

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8 (a)

I hereby certify that the attached document is being deposited with the United States Postal Services as First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231 on the date indicated below.

3-15-00



Jackie McBride
Jackie McBride

13
1001